

all holes

$\oplus \quad \varnothing 0,05$

$\varnothing 1 \pm 0,1$

position — 10

1

a

c

row

2,54

2x 2,54 (=5,08)

2,54

9 x 2,54 (=22,86)

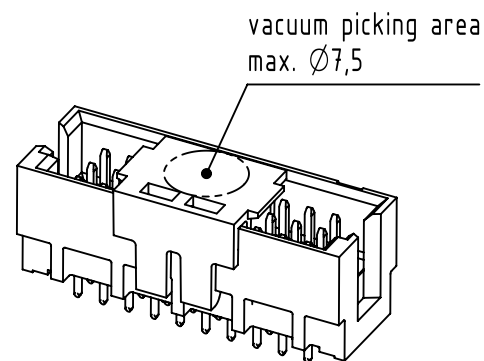
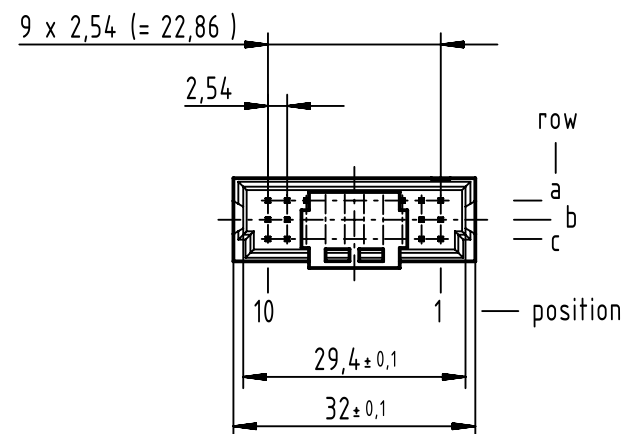
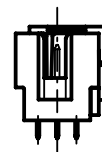
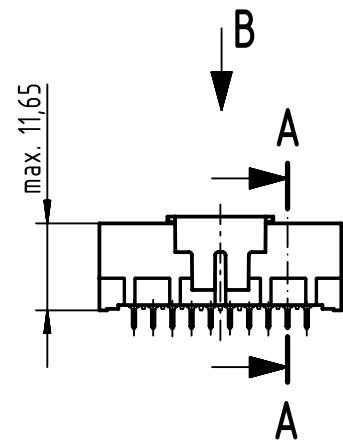


Diagram illustrating a memory layout (Figure 1.10). The layout is a 3x10 grid of cells, labeled 'row' and 'position'.

The rows are labeled 'a', 'b', and 'c' on the right. The positions are labeled 10, 9, 8, 7, 6, 5, 4, 3, 2, 1 from left to right.

Each cell in the grid contains a letter 'B'.

1 further information see: DS 09031230203		09 29 130 <u>6</u> 569 792		2			
		part-no.		performance level 1			
All Dimensions in mm Original Size DIN A3		Scale 1:1	Free size tol. complementary IEC 60603-2			Ref. Sub.	
All rights reserved Department EC PD - DE		Created by HASSE		Inspected by JASCHKE	Standardisation HOFFMANN	Date 2014-04-17	State Final Release
HARTING Electronics GmbH D-32339 Espelkamp		Title DIN Signal type 3R male solder SMC 2.5mm 30-pole					Doc-Key / ECM-Nr. 100223818/UGD/029/A 500000073969
		Type TB	Number 0929130X569792				Rev. A